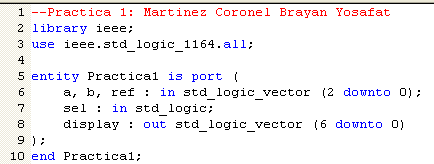
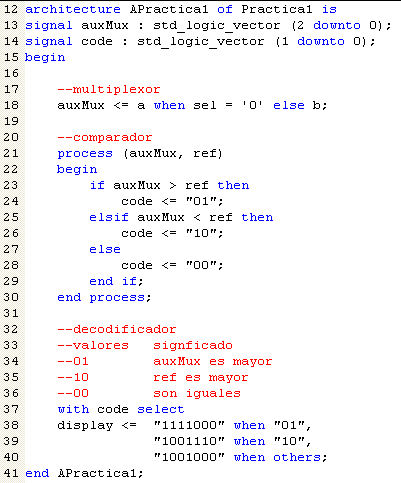
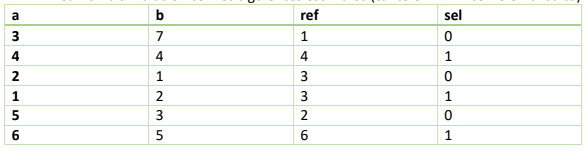
# Práctica 1: Martínez Coronel Brayan Yosafat

Realizar las simulaciones con los siguientes estímulos

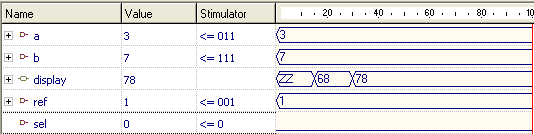


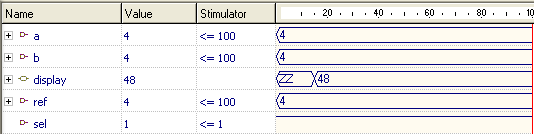


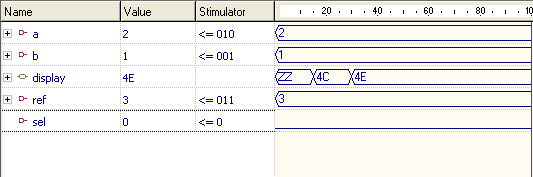


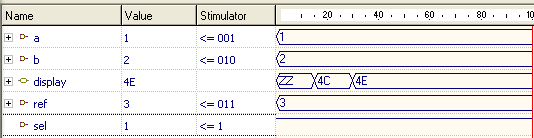
Simulaciones del VHDL:

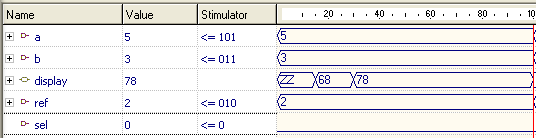
Significado: 78 = Mayor que 4E = Menor que 48 = Igual

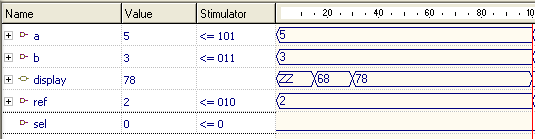


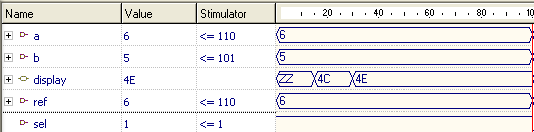


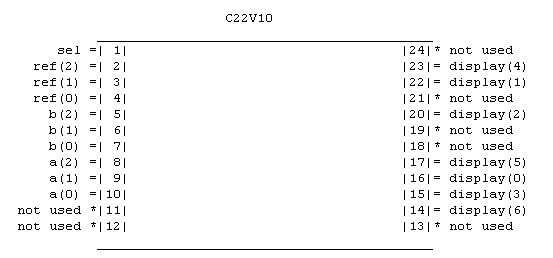












Simulaciones en Proteus

1111000 = Mayor que 1001110 = Menor que 1001000 = Igual

